

Attorney Docket No. 5649-1169

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Joung-Yeal Kim

Application No.: 10/701,321 Filed:

Group Art Unit: 2819 Examiner: Anh Q. Tran **PATENT**

November 4, 2003 For: OUTPUT BUFFER CIRCUITS INCLUDING LOGIC GATES HAVING BALANCED

OUTPUT NODES

Date: June 7, 2005

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT **PURSUANT TO 37 C.F.R. §1.97(c)**

Sir:

Attached is a list of documents on Form PTO-1449, together with a copy of any listed foreign patent document and/or non-patent literature. A copy of any listed U.S. patent and/or U.S. patent application publication is not provided herewith in accordance with the amendment by the U.S. Patent and Trademark Office to 37 C.F.R. § 1.98(a)(2)(ii) effective October 21, 2004.

This Information Disclosure Statement is submitted in accordance with 37 C.F.R. § 1.97(c), before final Office Action or Allowance, whichever is earlier.

In accordance with the requirements of 37 C.F.R. § 1.97(c)(1), the following Certification as specified in 37 C.F.R. § 1.97(e) is made:

In accordance with the requirements of 37 C.F.R. § 1.97(c)(2), a check for the \$180.00 fee specified in 37 C.F.R. § 1.17(p) is enclosed. This amount is believed to be correct. However, the Commissioner is authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

It is requested that these documents be considered by the Examiner and officially made of record in accordance with the provisions of 37 C.F.R. §1.56 and Section 609 of the MPEP.

Respectfully submitted,

Registration Nb. 38,176

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Certificate of Mailing under 37 CFR 1.8 [or under 37 CFR 1.10 -see above] I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450/on June 7, 2005.

Joyce Pao

Substitute form 1449A/PTO OCT 2 0 2005 WINFORMATION DISCLOSURE STATEMENT BY APPLICATION OF THE STATEMENT BY APPLICATION OF THE

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Complete if Known				
Application Number	10/701,321			
Filing Date	November 4, 2003			
First Named Inventor	Joung-Yeal Kim			
Group Art Unit	2819			
Examiner Name	Anh Q. Tran			
Attorney Docket Number	5649-1169			

U.S. PATENTS AND PATENT PUBLICATIONS							
Examiner Ci Initials*	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document		
		Number	(if known)	Document	MM-DD-YYYY		
	1.	US-6,229,336		Felton et al;	05/08/2001		
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	Cite No.	Office	Foreign Patent Do	cument	Name of Patentee or Applicant of Cited	I Date of	Translation
2.		Office			Document	Date of Publication of	Translation
			Number	Kind Code	Dodanient	Cited	
			(if known)		Document		
	·					MM-DD-YYYY	
1 2	2.		11202970		Japanese Publication	07/30/1999	
3.	3.		11202970		English Translation of Japanese Abstract	07/30/1999	
				-			
					T LITERATURE DOCUMENTS		
	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published					
4.	I .	Korean Office Action for Application No. 10-2002-0067745.					
5.	5.	English Translation of Korean Office Action for Application No. 10-2002-0067745.					
6.	6. Logic Gates-Digital Logic Circuits "Truth Table for the "NOR" Gate";						
5.							
7.	http://www.autoshop101.com/trainmodules/logicgate/127.html. 7. Logic Gates-Digital Logic Circuits "Truth Table for the "NAND" Gate";						
		http://w	ww.autoshop101.d	com/trainmodule	s/logicgat/121.html.		

Examiner Signature	Date Considered	